VS-130-160MT..KPbF Series



Vishay Semiconductors

Three Phase Bridge, 130 A to 160 A (Power Modules)



PRODUCT SUMMARY			
I _O	130 A to 160 A		
V _{RRM}	800 V to 1600 V		
Package	MT-K		
Circuit	Three phase bridge		

FEATURES

 Package fully compatible with the industry standard INT-A-PAK power modules series



RoHS COMPLIANT

- High thermal conductivity package, electrically insulated case
- Excellent power volume ratio
- 4000 V_{RMS} isolating voltage
- UL E78996 approved
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

A range of extremely compact, encapsulated three phase bridge rectifiers offering efficient and reliable operation. They are intended for use in general purpose and heavy duty applications.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES 130MT.K	VALUES 160MT.K	UNITS	
1		130 (160)	160 (200)	А	
I _O	T _C	85 (62)	85 (60)	°C	
1	50 Hz	1130	1430	- A	
I _{FSM}	60 Hz	1180	1500		
l ² t	50 Hz	6400	10 200	– A ² s	
14	60 Hz	5800	9300		
l²√t		64 000	102 000	A²√s	
V _{RRM}	Range	800 to 1600		V	
T _{Stg}	Panga	-40 to 150		°C	
TJ	Range			U	

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS				
TYPE NUMBER	VOLTAGE CODE VRRM, MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V		V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = MAXIMUM mA
VS-130-160MTK	80	800	900	
	100	1000	1100	
	120	1200	1300	10
	140	1400	1500	
	160	1600	1700	

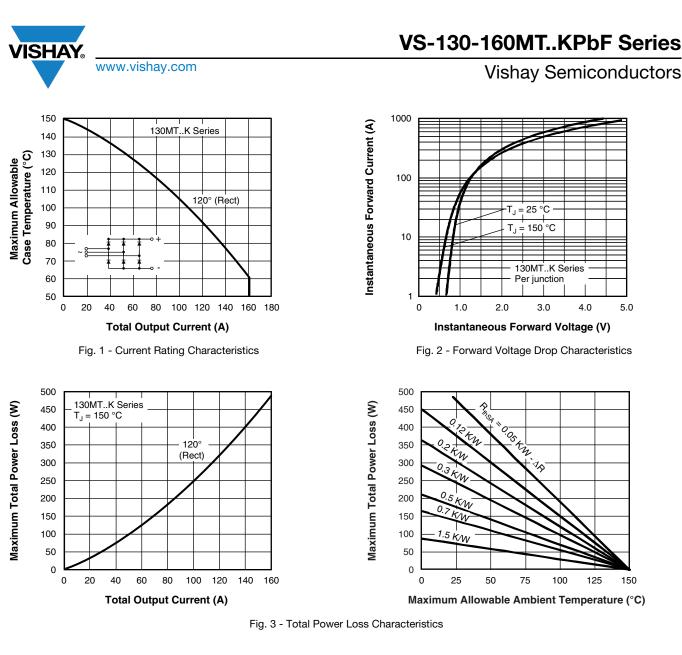
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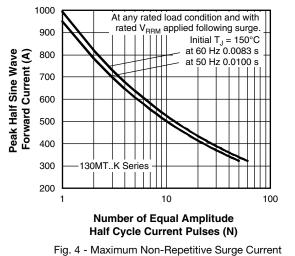


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FORWARD CONDUCTION							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES 130MT.K	VALUES 160MT.K	UNITS	
Maximum DC output current	lo	120° rect. conduction angle		130 (160)	160 (200)	А	
at case temperature	ю	120 1601.0	onduction any		85 (62)	85 (60)	°C
Maximum peak, one-cycle	I _{TSM}	t = 10 ms	No voltage	-	1130	1430	A
		t = 8.3 ms	reapplied		1180	1500	
forward, non-repetitive surge current		t = 10 ms	100 % V _{RRM}		950	1200	
		t = 8.3 ms	reapplied	Initial	1000	1260	
Maximum I ² t for fusing		t = 10 ms	No voltage	T _J = T _J maximum	64 000	102 000	A ² s
	l ² t	t = 8.3 ms	reapplied		5800	9300	
		t = 10 ms	100 % V _{BBM}		4500	7200	
		t = 8.3 ms	reapplied		4100	6600	
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied		64 000	102 000	A²√s	
Low level value of threshold voltage	V _{T(TO)1}	(16.7 % x π x I _{T(AV)} < I < π x I _{T(AV)}), T _J maximum		0.78	0.81	v	
High level value of threshold voltage	V _{T(TO)2}	$(I > \pi \times I_{T(AV)}), T_J$ maximum		0.99	1.04	v	
Low level value of forward slope resistance	r _{f1}	16.7 % x π x I _{T(AV)} < I < π x I _{T(AV)}), T _J maximum		4.59	3.52	mΩ	
High level of forward slope resistance	r _{f2}	$(I > \pi \times I_{T(AV)}), T_J$ maximum		4.17	3.13	11152	
Maximum forward voltage drop	V _{FM}	I_{pk} = 200 A, T_J = 25 °C, t_p = 400 μs single junction		1.63	1.49	v	
RMS isolation voltage	V _{ISOL}	$T_J = 25 \text{ °C}$, all terminal shorted f = 50 Hz, t = 1 s		40	000	v	

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES 130MT.K	VALUES 160MT.K	UNITS
Maximum junction operating and storage temperature range	T _J , T _{Stg}		-40 t	o 150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation per module	0.16	0.12	ĸ/w
		DC operation per junction	0.93	0.73	
		120° rect. condunction angle per module	0.18	0.15	
		120° rect. condunction angle per junction	1.08	0.88	
Maximum thermal resistance, case to heatsink	R _{thCS}	Per module Mounting surface smooth, flat and greased	0.03		
Mounting to heatsink		A mounting compound is recommended	4 t	o 6	Nm
torque ± 10 % to terminal		and the torque should be rechecked after a period of 3 hours to allow for the spread	3 to 4		INIII
Approximate weight		of the compound. Lubricated threads.	176		g





1200 Maximum non-repetitive surge current 1100 versus pulse train duration. Initial T_{.1} = 150 °C 1000 No voltage reapplied Peak Half Sine Wave Forward Current (A) Rated V_{RRM} reapplied 900 800 700 600 500 400 130MT..K Series 300 200 0.01 0.1 1.0 Pulse Train Duration (s)

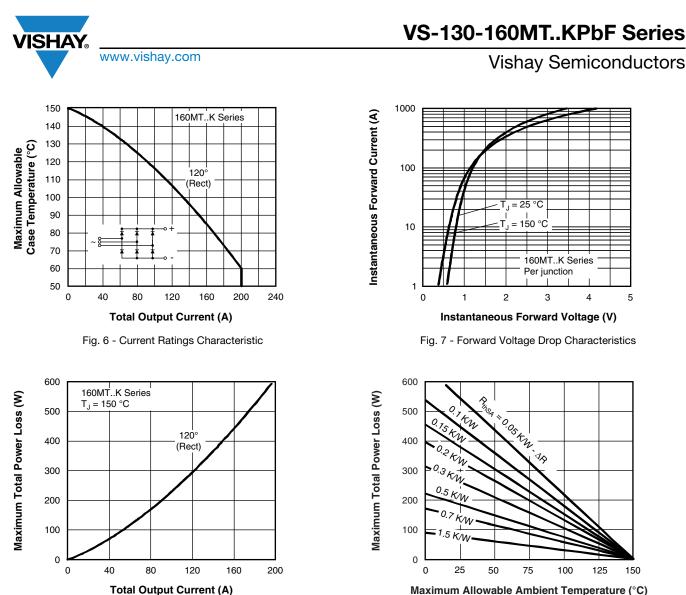
Fig. 5 - Maximum Non-Repetitive Surge Current

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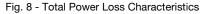
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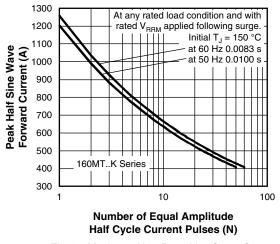


Fig. 9 - Maximum Non-Repetitive Surge Current

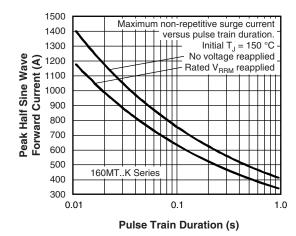


Fig. 10 - Maximum Non-Repetitive Surge Current

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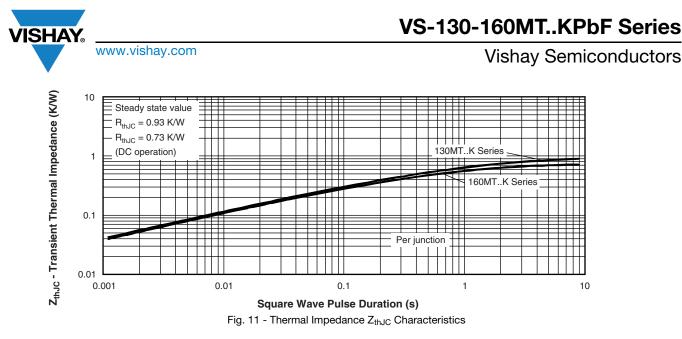
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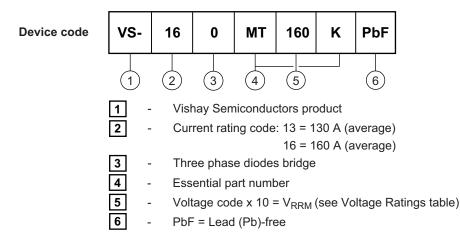
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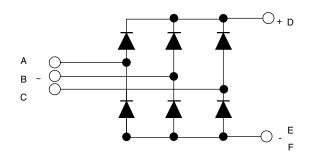
ORDERING INFORMATION TABLE



Note

To order the optional hardware go to: <u>www.vishay.com/doc?95172</u>

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95004		

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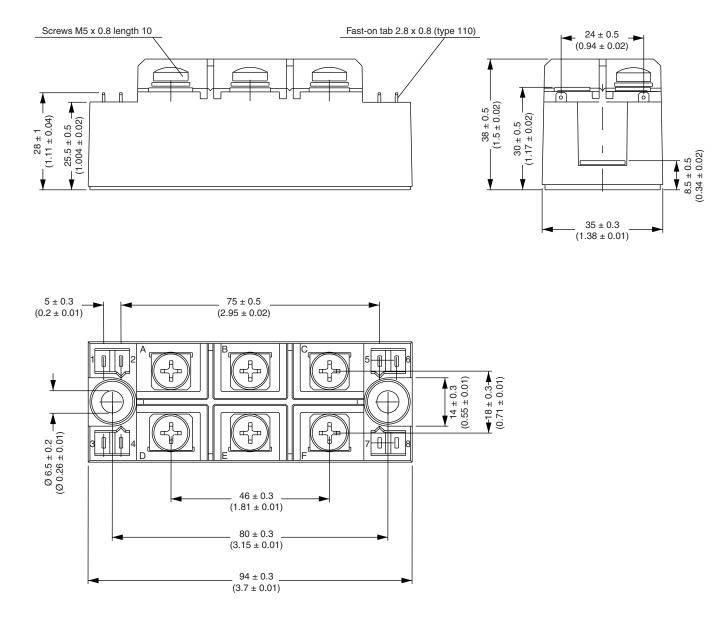


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MTK (with and without optional barrier)

DIMENSIONS WITH OPTIONAL BARRIERS in millimeters (inches)

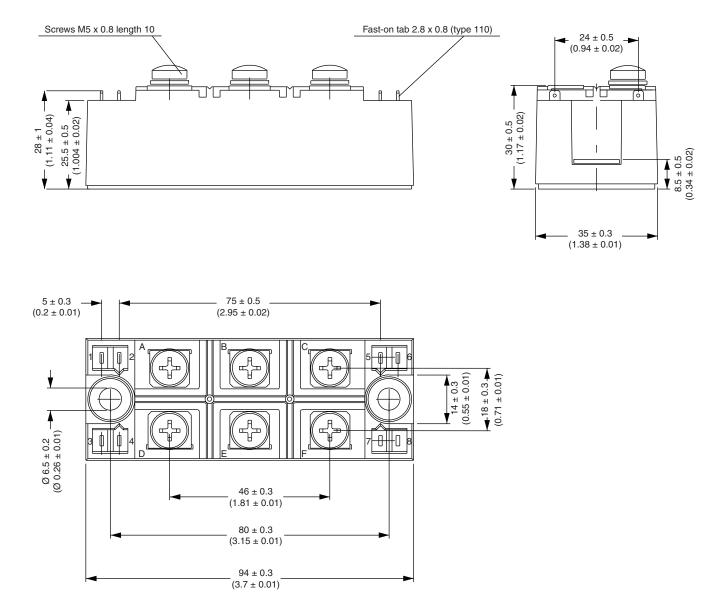
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Vishay Semiconductors MTK (with and without optional barrier)



DIMENSIONS WITHOUT OPTIONAL BARRIERS in millimeters (inches)





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