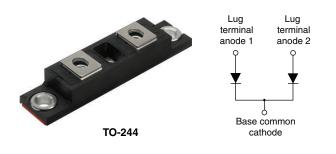
**Vishay Semiconductors** 

## High Performance Schottky Rectifier, 200 A



200 A

135 V, 150 V

TO-244

Two diodes common cathodes

FE/	ATU	IRES	•
		mE3	,

- 175 °C T<sub>J</sub> operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### DESCRIPTION

The VS-209CNQ center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN				
I <sub>F(AV)</sub>	Rectangular waveform	200	А			
V <sub>RRM</sub>	Range	135/150	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	10 000	А			
V <sub>F</sub>	100 A <sub>pk</sub> , $T_J = 125$ °C (per leg)	0.71	V			
TJ	Range	-55 to 175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-209CNQ135PbF	VS-209CNQ150PbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	135	150	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	155	150		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current per leg	1	50 % duty cycle at $T_C$ = 131 °C, rectangular waveform		100	
See fig. 5 per device	IF(AV)			200	
Maximum peak one cycle non-repetitive surge	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and	10 000	A
current per leg See fig. 7		10 ms sine or 6 ms rect. pulse	with rated V <sub>RRM</sub> applied	1200	
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 5.5 A, L = 1 mH		15	mJ
Repetitive avalanche current per leg		Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1	А

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COMPLIANT



**PRODUCT SUMMARY** 

I<sub>F(AV)</sub>

 $V_R$ 

Package

Circuit



### Vishay Semiconductors

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
	V <sub>FM</sub> <sup>(1)</sup>	100 A	T <sub>.1</sub> = 25 °C	1.06	V
Maximum forwardvoltage drop per leg		200 A	1j=25 0	1.33	
See fig. 1		100 A	T 105 %C	0.74	
		200 A	T <sub>J</sub> = 125 °C	0.88	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	3	mA
See fig. 2		T <sub>J</sub> = 125 °C	V <sub>R</sub> = naled V <sub>R</sub>	45	
Maximum junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 $^{\circ}{\rm C}$		3000	pF
Typical series inductance per leg	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs	

Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>	-55	-	175	°C
Thermal resistance, junction to case	per leg	R <sub>thJC</sub>	-	-	0.38	°C/W
memai resistance, junction to case	per module	nthJC	-	-	0.19	
Thermal resistance, case to heatsink		R <sub>thCS</sub>	-	0.10	-	
			68		g	
Weight			-	2.4	-	oz.
Mounting torque			35.4 (4)	-	53.1 (6)	
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)
Terminal torque			30 (3.4)	-	44.2 (5)	(,
Vertical pull 2" lever pull			-	-	80	11-11-11-
			-	-	35	- Ibf ⋅ in

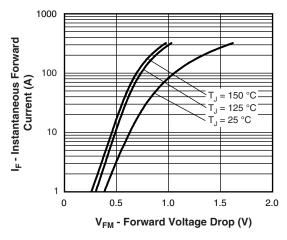


Fig. 1 - Maximum Forward Voltage Drop Characteristics

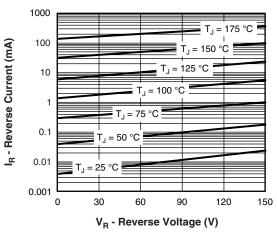


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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## VS-209CNQ...PbF Series

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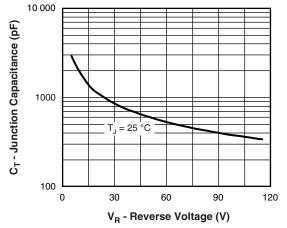


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

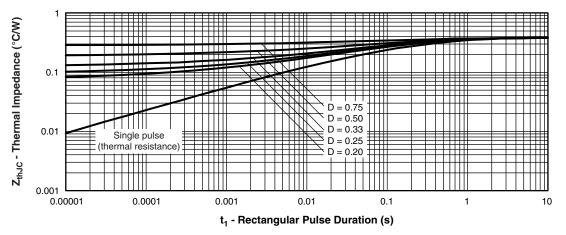
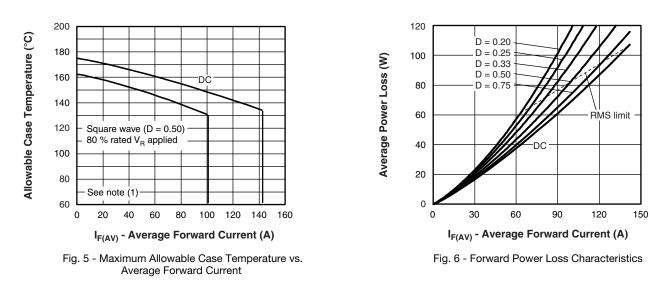


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics



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## VS-209CNQ...PbF Series

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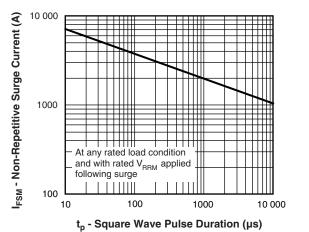


Fig. 7 - Maximum Non-Repetitive Surge Current

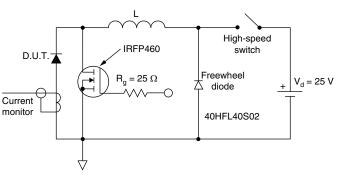
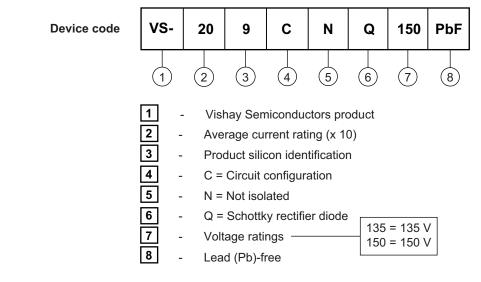


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R at V_{R1} = 80 \%$  rated  $V_R$ 

### **ORDERING INFORMATION TABLE**



#### LINKS TO BELATED DOCUMENTS

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Dimensions	www.vishay.com/doc?95021			

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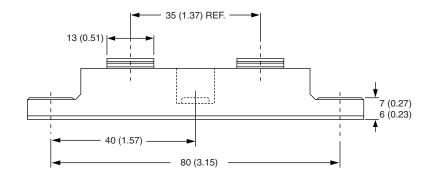


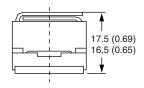


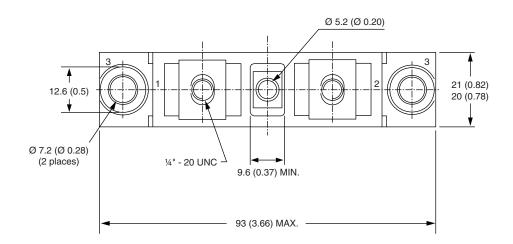
**Vishay Semiconductors** 

**TO-244** 

### **DIMENSIONS** in millimeters (inches)









Vishay

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